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APPLICANTS

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** CONTINUING DATA ***** *NO* *****

** FOREIGN APPLICATIONS ***** *NO* *****
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>Phaw Bory</i> Acknowledged <u> </u> TP Allowance Examiner's Signature Initials	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 5	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 4
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TITLE

Semiconductor memory device and layout method thereof

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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